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FIG. 1
PRIOR ART

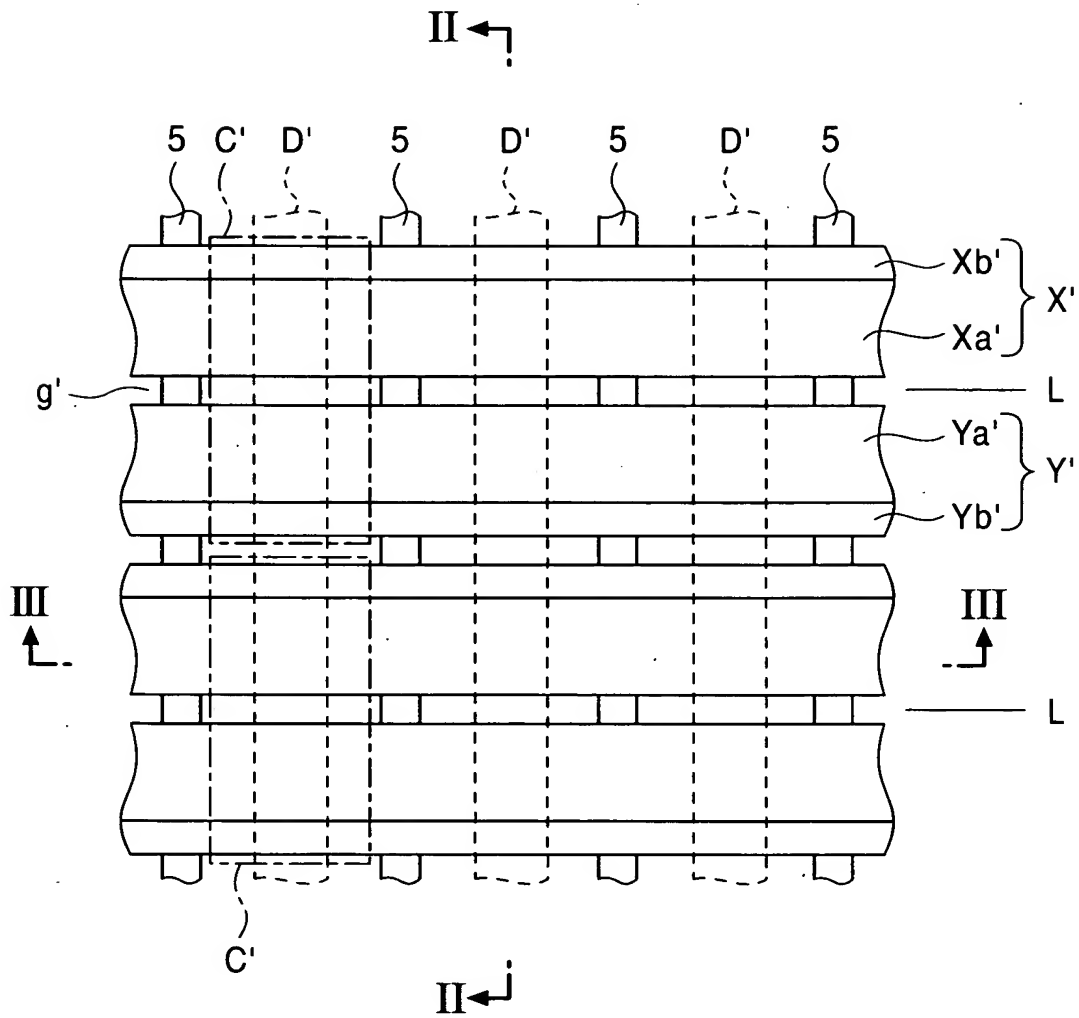

FIG. 2
PRIOR ART

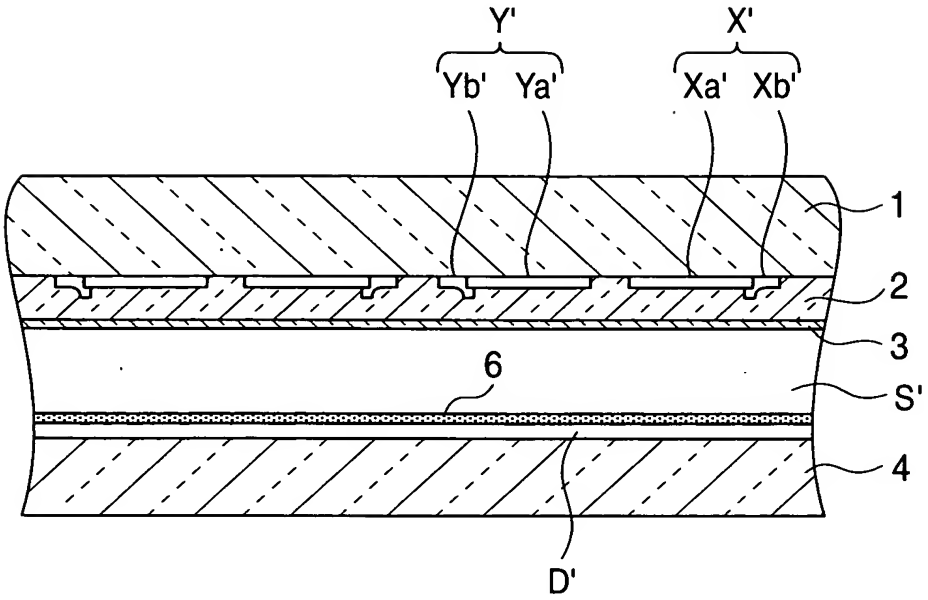

FIG. 3
PRIOR ART

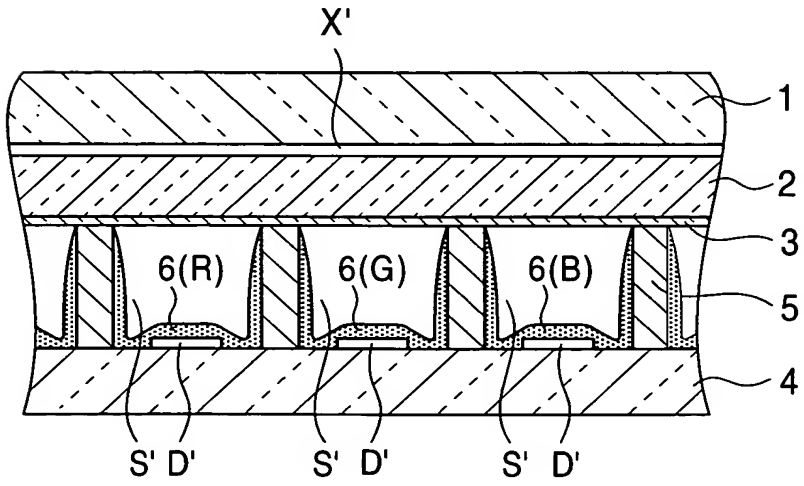

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FIG. 4
PRIOR ART

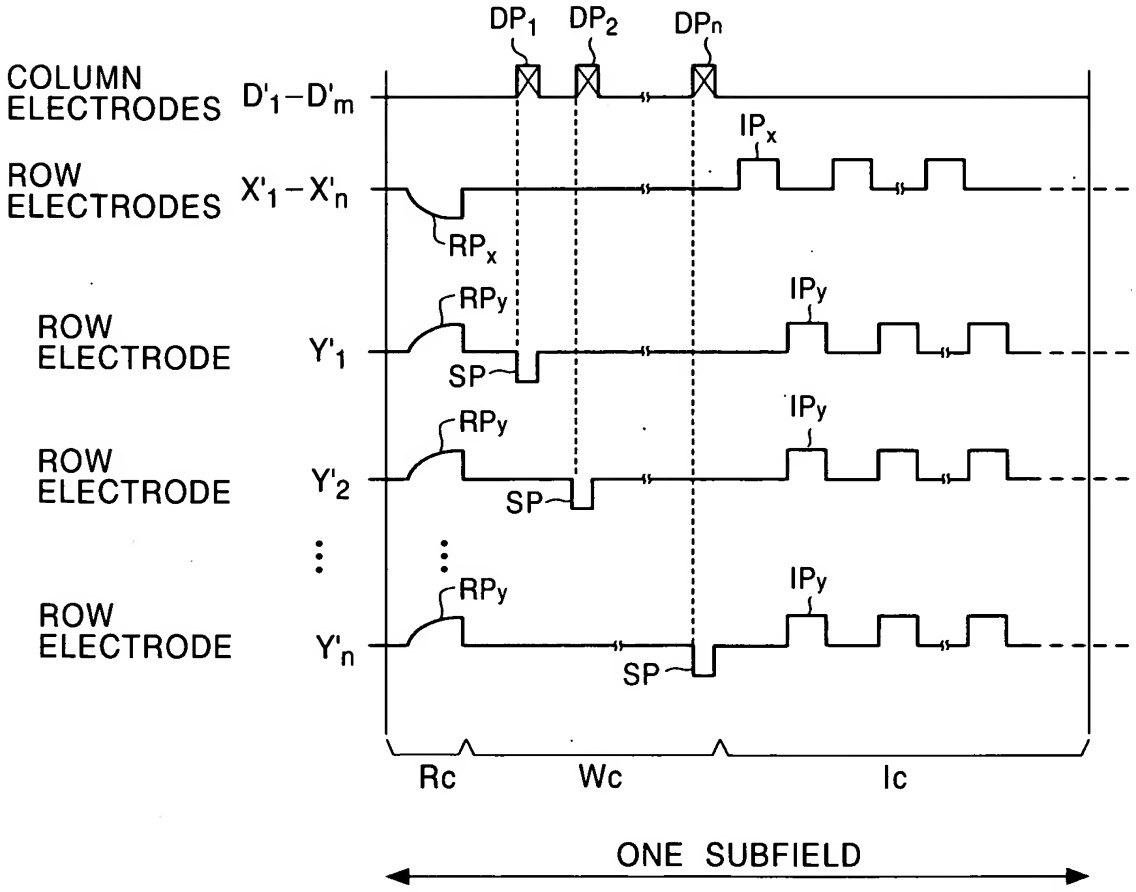

FIG. 5

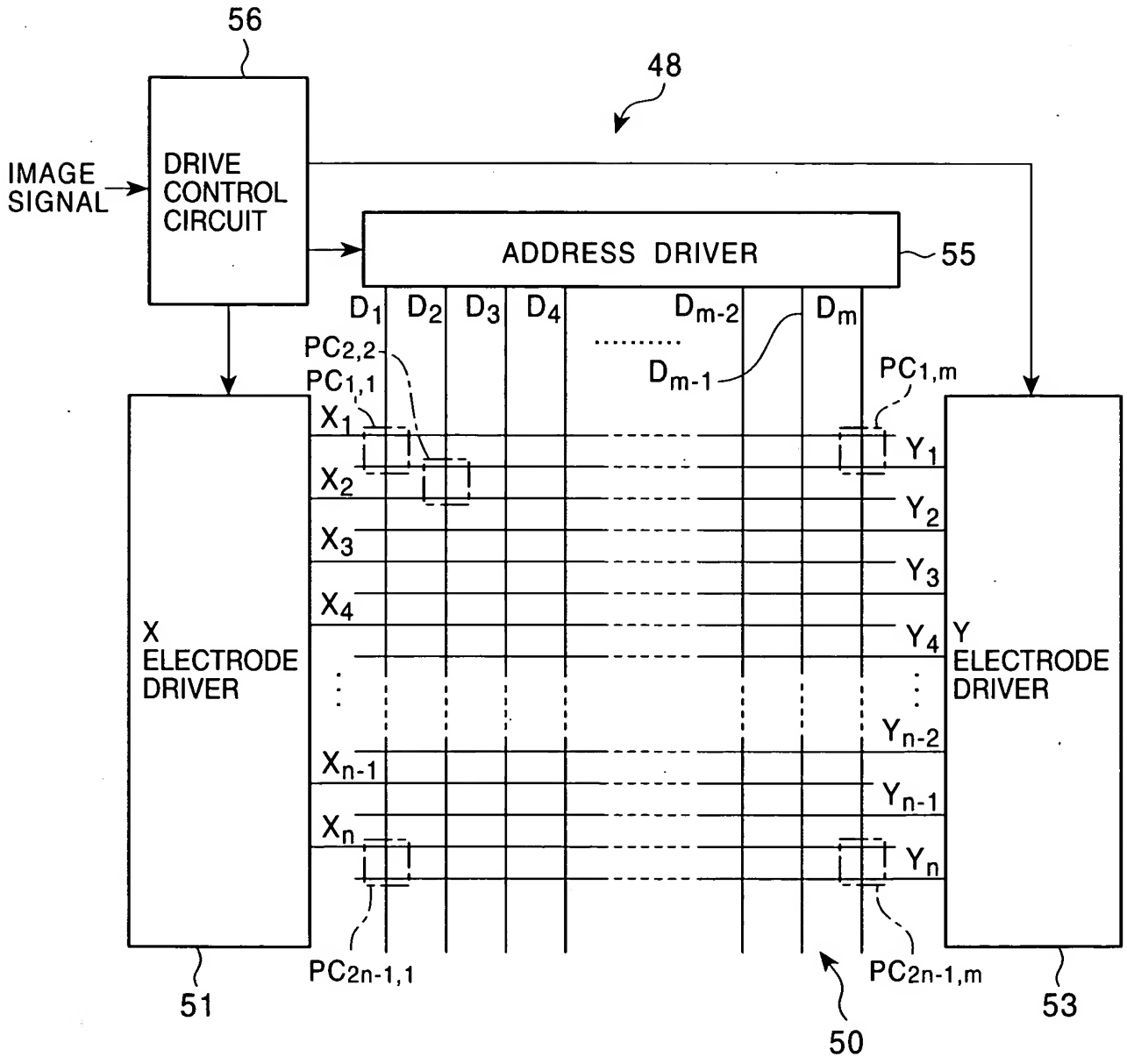
56
48
IMAGE SIGNAL
DRIVE CONTROL CIRCUIT
ADDRESS DRIVER
55
D1 D2 D3 D4 Dm-2 Dm-1 Dm
PC2,2
PC1,1
PC1,m
X1 X2 X3 X4 Xn-1 Xn
Y1 Y2 Y3 Y4 Yn-2 Yn-1 Yn
X ELECTRODE DRIVER
Y ELECTRODE DRIVER
PC2n-1,1
PC2n-1,m
51
50
53

FIG. 6

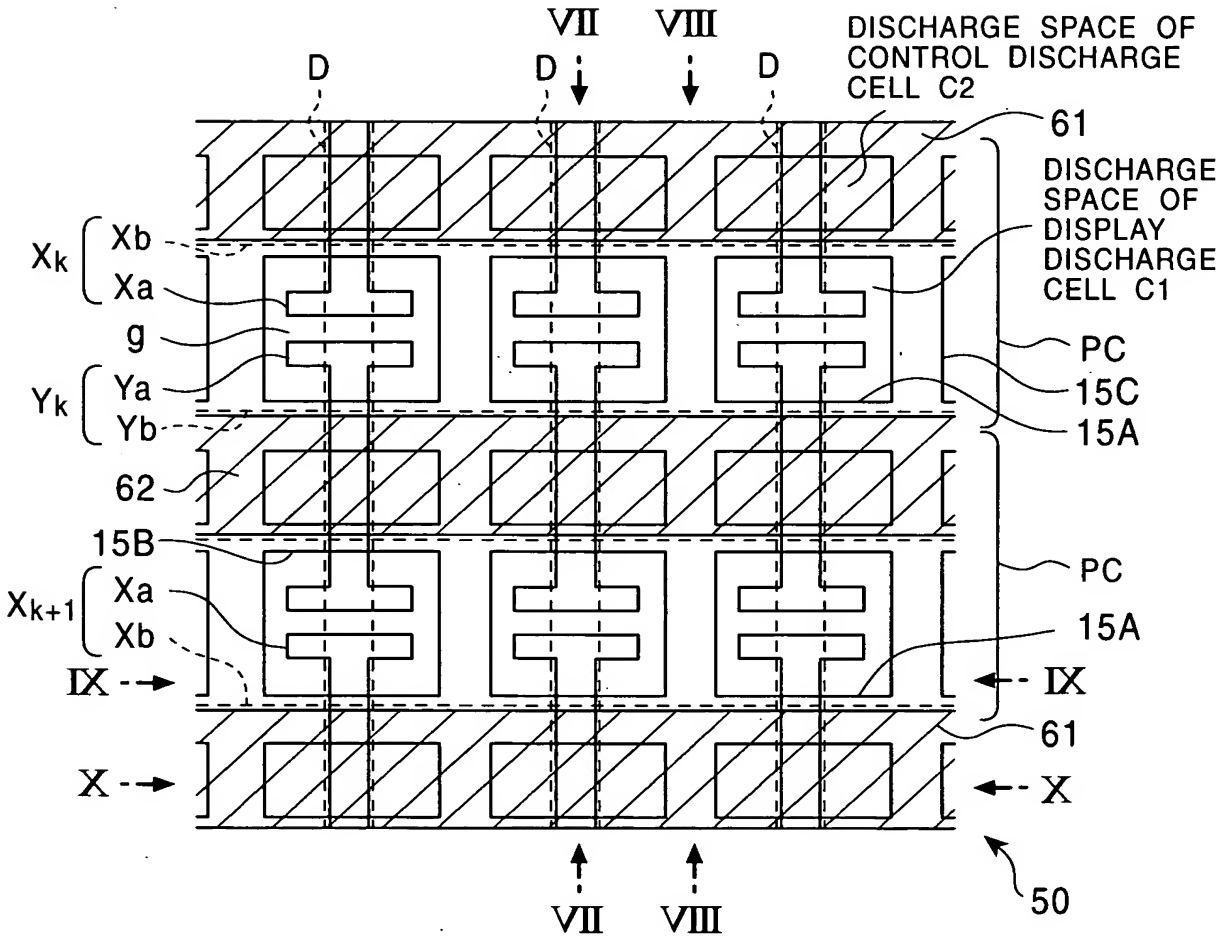
VII
VIII
D
D
D
DISCHARGE SPACE OF CONTROL DISCHARGE CELL C2
61
DISCHARGE SPACE OF DISPLAY DISCHARGE CELL C1
Xk
Xb
Xa
g
Yk
Ya
Yb
62
15B
Xk+1
Xa
Xb
IX
X
PC
15C
15A
PC
15A
IX
61
X
50
VII
VIII

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FIG. 7

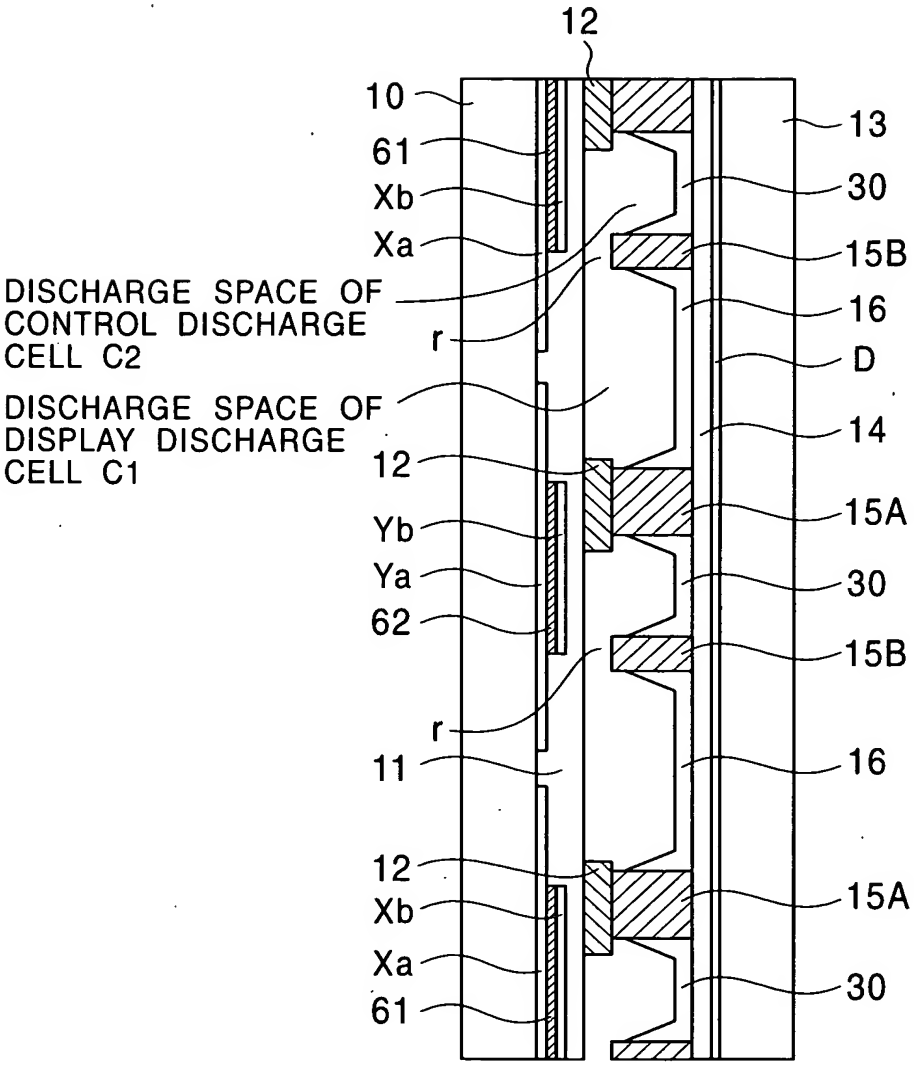

FIG. 8

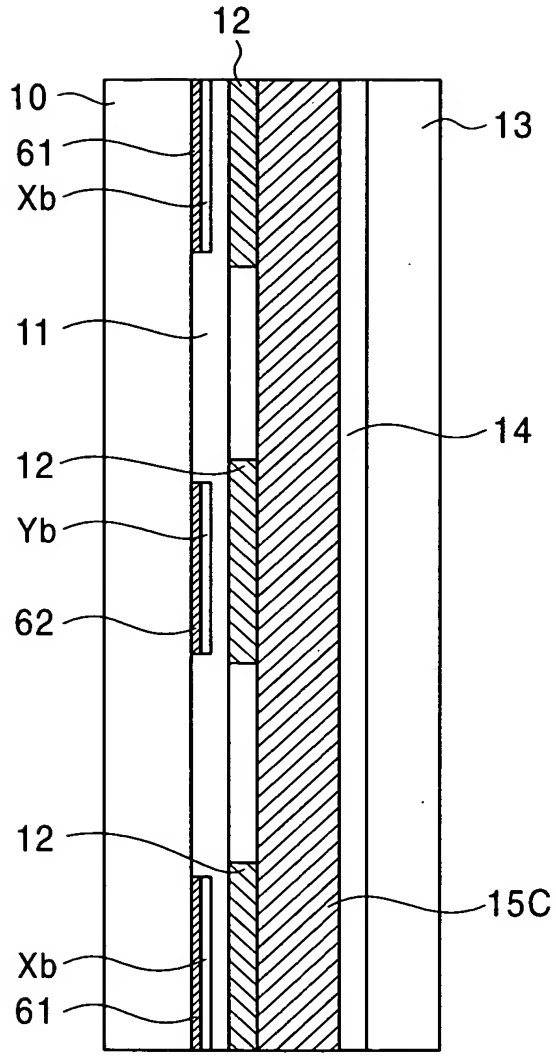

FIG. 9

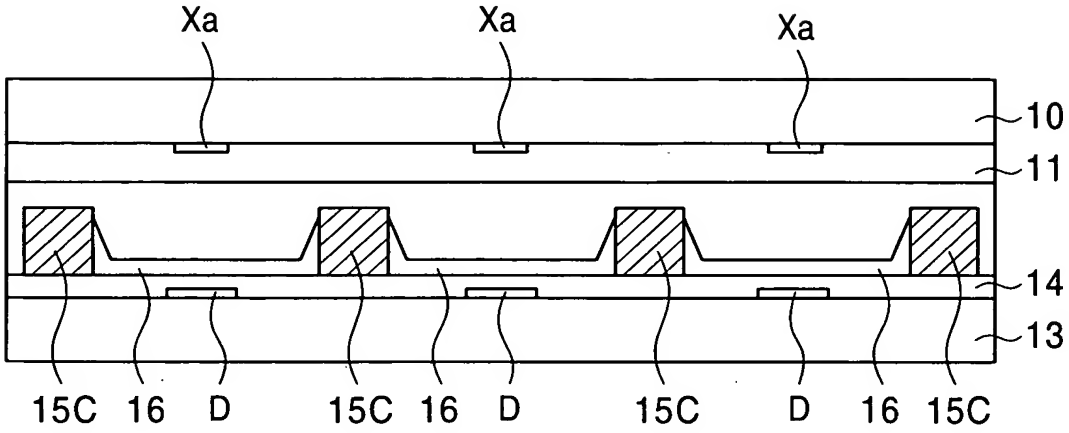
Xa
Xa
Xa
10
11
14
13
15C
16
D
15C
16
D
15C
D
16
15C

FIG. 10

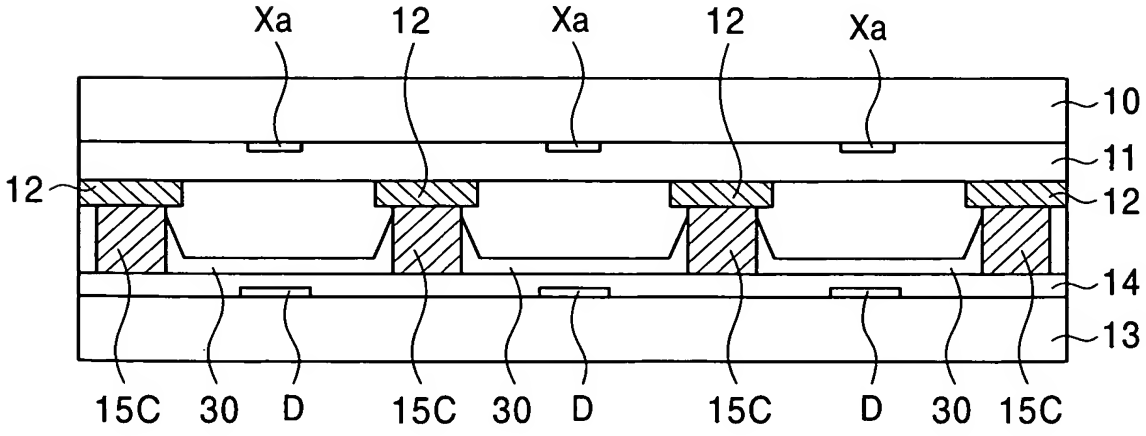
Xa
12
Xa
12
Xa
10
11
12
12
14
13
15C
30
D
15C
30
D
15C
D
30
15C

FIG. 11

GRADATION DRIVING	CONVERSION TABLE																LIGHT EMISSION PATTERN															
	PDs	GD															SF	SF	SF	SF	SF	SF	SF	SF	SF	SF	SF	SF	SF	SF	SF	SF
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
1ST LEVEL	0000	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	●															
2ND LEVEL	0001	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	○	●														
3RD LEVEL	0010	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	○	○	●													
4TH LEVEL	0011	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	○	○	○	●												
5TH LEVEL	0100	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	○	○	○	○	●											
6TH LEVEL	0101	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	○	○	○	○	○	●										
7TH LEVEL	0110	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	○	○	○	○	○	○	●									
8TH LEVEL	0111	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	○	○	○	○	○	○	○	●								
9TH LEVEL	1000	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	○	○	○	○	○	○	○	○	●							
10TH LEVEL	1001	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	○	○	○	○	○	○	○	○	○	●						
11TH LEVEL	1010	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	○	○	○	○	○	○	○	○	○	○	●					
12TH LEVEL	1011	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	○	○	○	○	○	○	○	○	○	○	○	●				
13TH LEVEL	1100	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	○	○	○	○	○	○	○	○	○	○	○	○	●			
14TH LEVEL	1101	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	○	○	○	○	○	○	○	○	○	○	○	○	○	●		
15TH LEVEL	1110	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	○	○	○	○	○	○	○	○	○	○	○	○	○	○	●	
16TH LEVEL	1111	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	○	○	○	○	○	○	○	○	○	○	○	○	○	○	○	○

● : EXTINCTION ADDRESS DISCHARGE

○ : SUSTAINING DISCHARGE LIGHT EMISSION

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FIG. 12

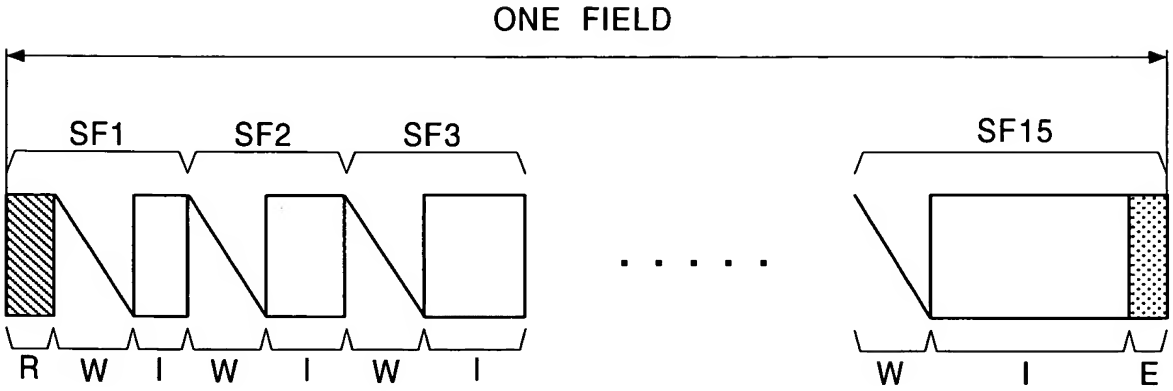

FIG. 13

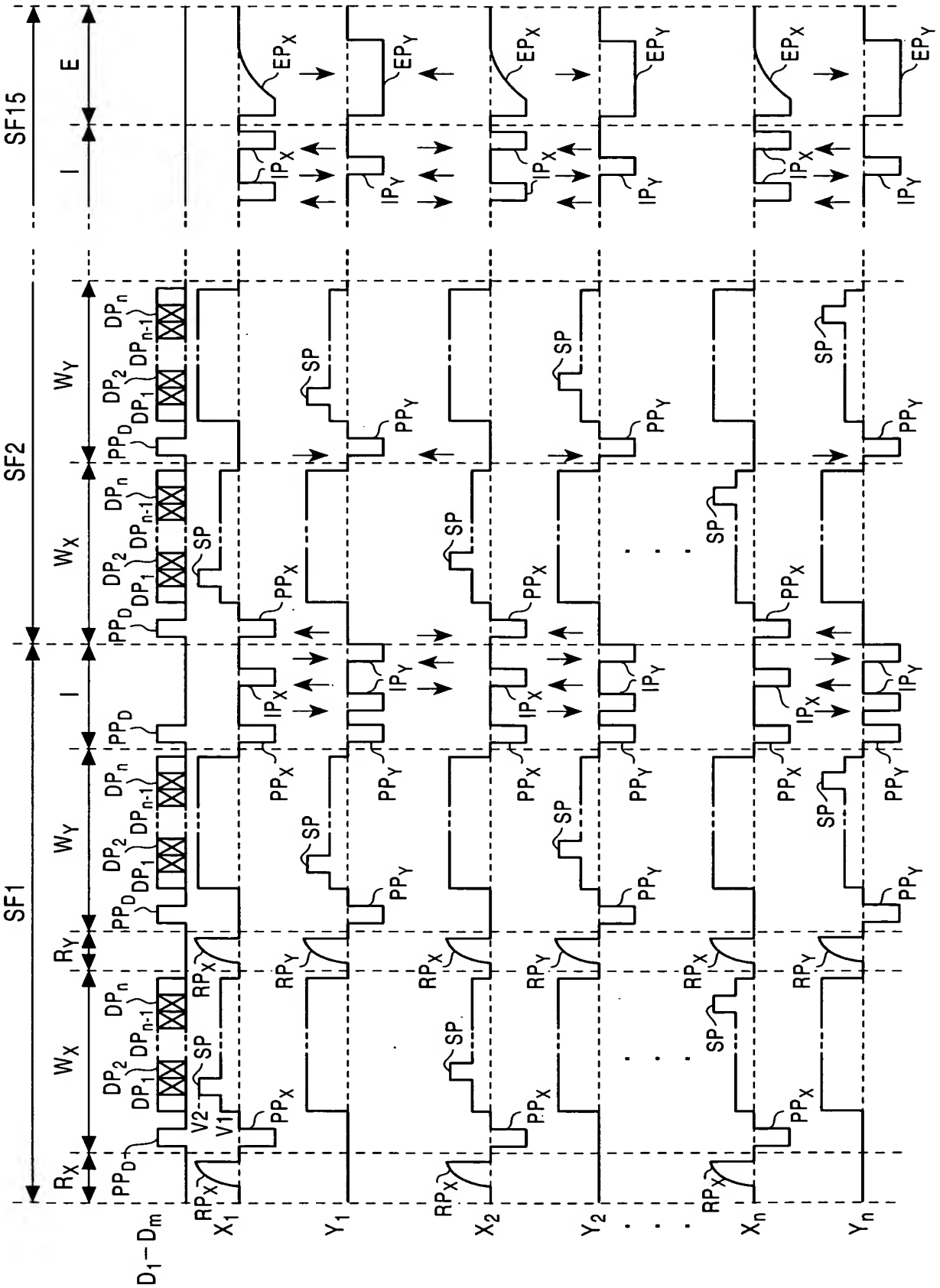
SF15
SF2
SF1
E
I
W_Y
W_X
R_Y
R_X
PP_D
DP_1
DP_2
DP_{n-1}
DP_n
SP
PP_X
PP_Y
RP_X
RP_Y
EP_X
EP_Y
IP_X
IP_Y
V2
V1
$D_1 - D_m$
X_1
Y_1
X_2
Y_2
X_n
Y_n

FIG. 14

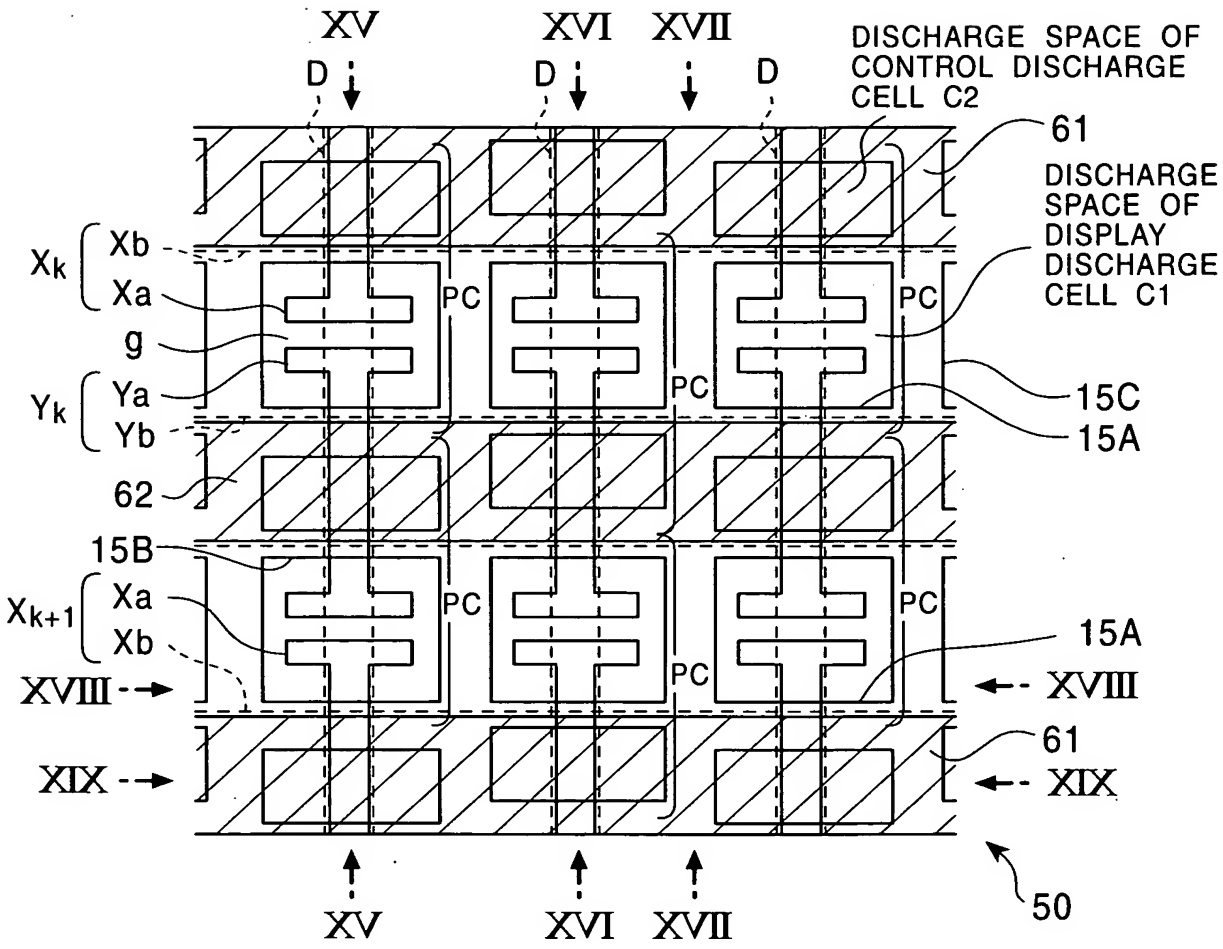
XV
XVI
XVII
D
D
D
DISCHARGE SPACE OF CONTROL DISCHARGE CELL C2
61
DISCHARGE SPACE OF DISPLAY DISCHARGE CELL C1
Xk
Xb
Xa
g
Yk
Ya
Yb
62
15B
Xk+1
Xa
Xb
XVIII
XIX
PC
PC
PC
PC
PC
PC
15C
15A
15A
XVIII
61
XIX
XV
XVI
XVII
50

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FIG. 15

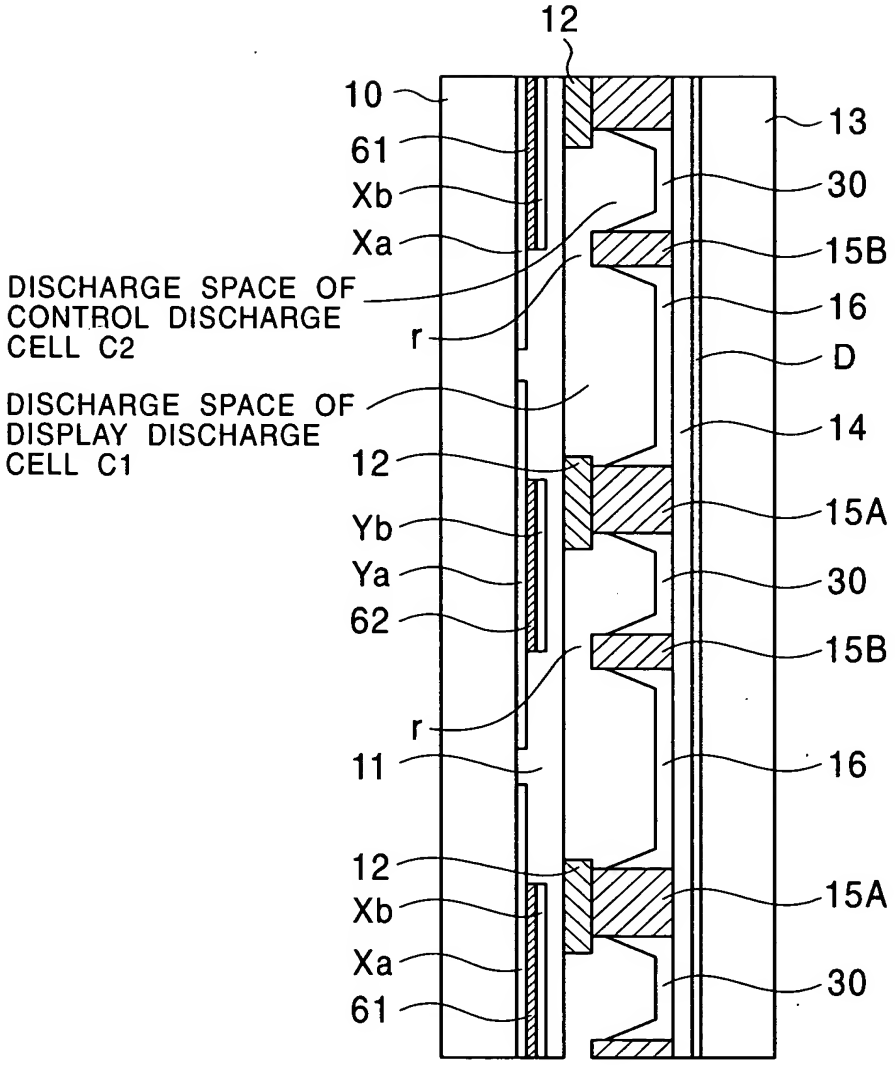
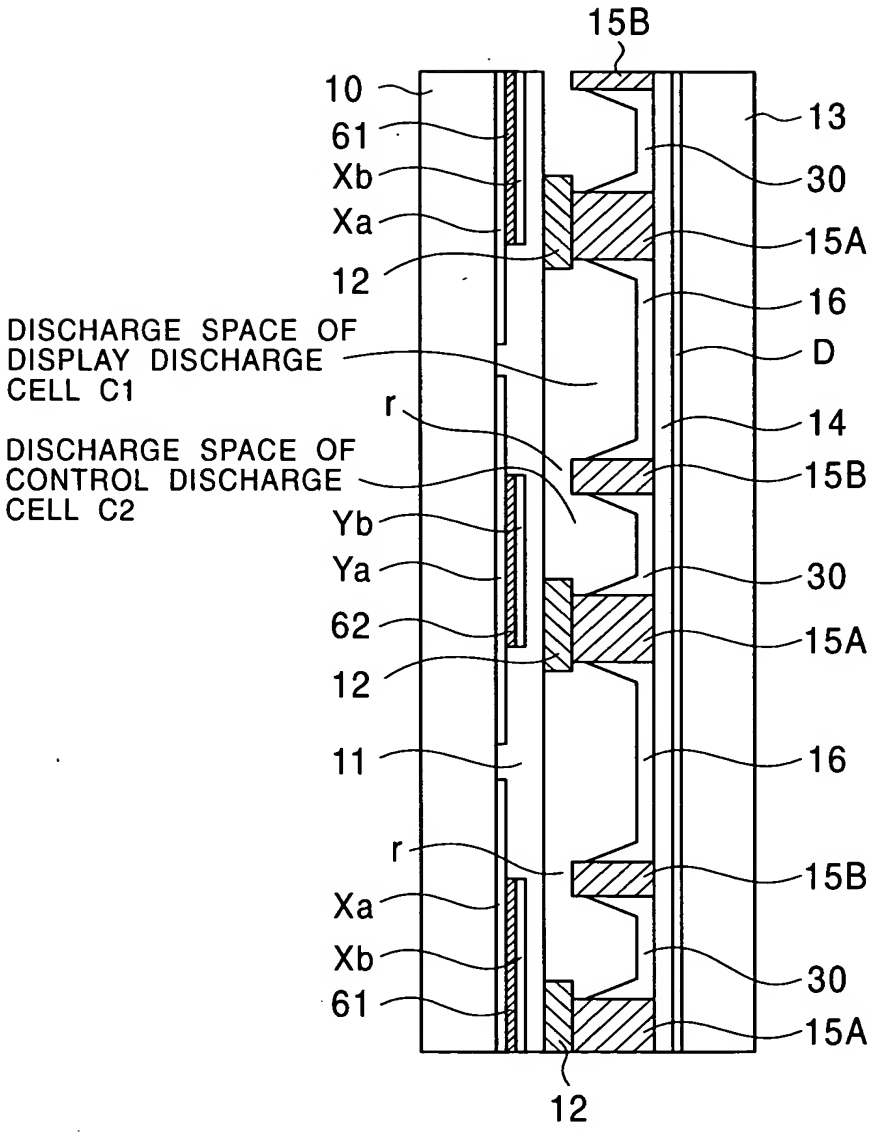


FIG. 16


15B
10
61
Xb
Xa
12
DISCHARGE SPACE OF DISPLAY DISCHARGE CELL C1
DISCHARGE SPACE OF CONTROL DISCHARGE CELL C2
r
Yb
Ya
62
12
11
r
Xa
Xb
61
12
13
30
15A
16
D
14
15B
30
15A
16
15B
30
15A

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15 0f 16

FIG. 17

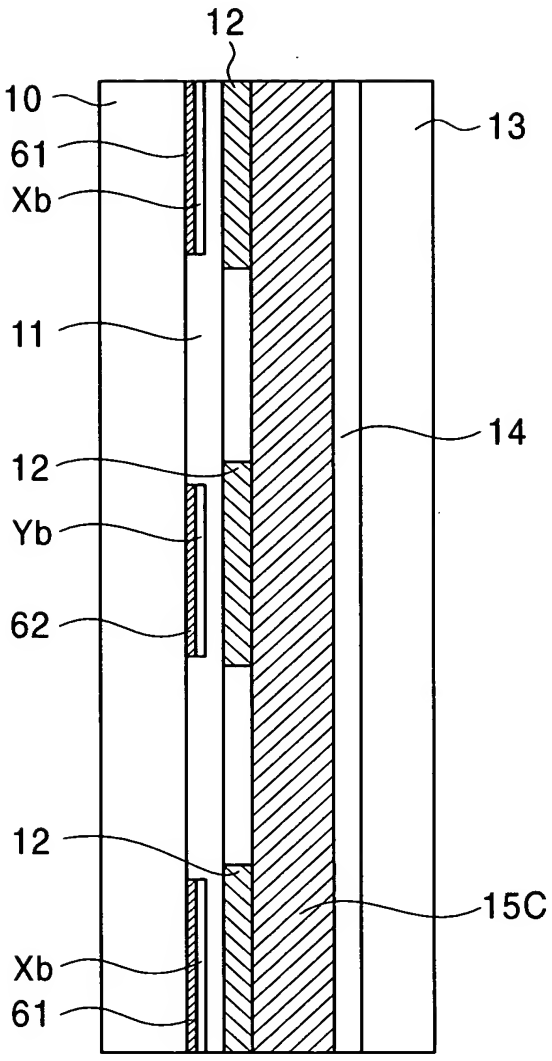


FIG. 18

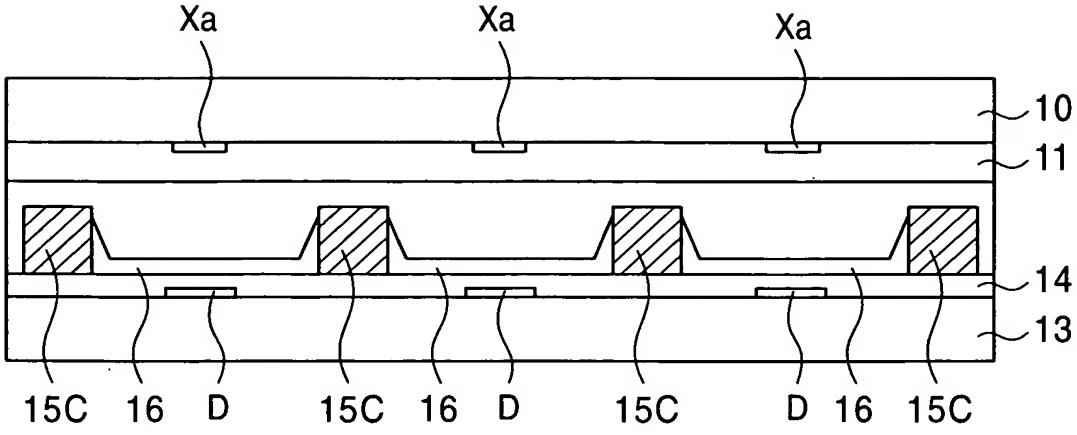
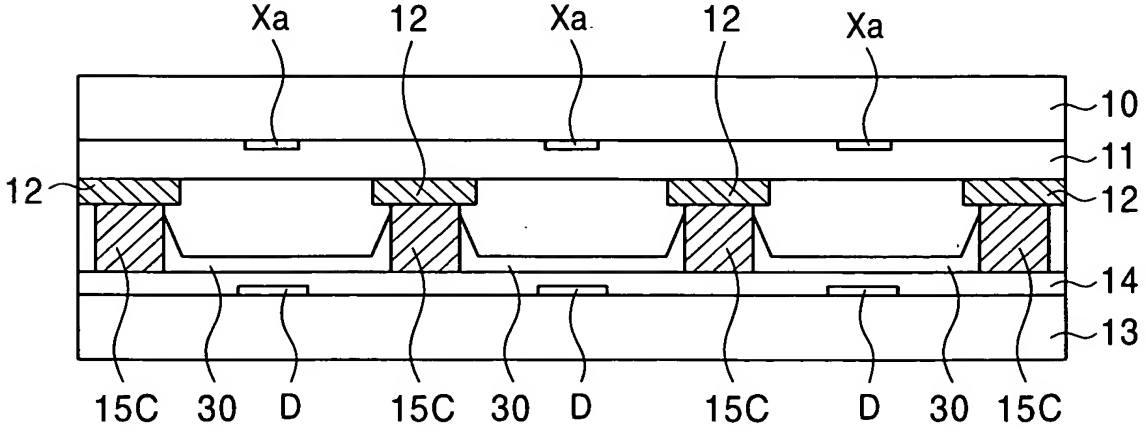

Xa
Xa
Xa
10
11
14
13
15C
16
D
15C
16
D
15C
D
16
15C

FIG. 19


Xa
12
Xa
12
Xa
10
11
12
12
14
13
15C
30
D
15C
30
D
15C
D
30
15C